

TW



Att. Docket No. NLMI.P213

PATENT

IN THE UNITED STATES PATENT OFFICE

Re Patent Application of

First Named Inventor: Srinivasan, Varadarajan

Application No.: 10/613,776

Filed: 7/2/2003

For: ROLLOVER BITS FOR PACKET DEPARTURE
TIME CALCULATOR

Examiner:

Art Unit: 2661

Confirmation 6968
No.

Mail Stop Amendment
Commissioner for Patents
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INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.97

Sir:

Enclosed is an Information Disclosure Citation Form 1449/PTO together with a copy of each reference cited therein, excluding U.S. Patents and Published U.S. Patent Applications. It is respectfully requested that the cited references be considered and that the enclosed copy of the Form 1449/PTO be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant.

This Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

 X 37 C.F.R. §1.97(b).

 37 C.F.R. §1.97(c).

 Authorization is hereby given to charge Deposit Account No. 501914 for the fee of \$180.00 under 37 C.F.R. §1.17(p) for the submission of this Information Disclosure Statement; or

 A statement pursuant to 37 C.F.R. §1.97(e) is attached hereto.

_____ 37 C.F.R. §1.97(d). Authorization is hereby given to charge Deposit Account No. 501914 for the fee of \$180.00 and a statement pursuant to 37 C.F.R. §1.97(e) is enclosed.

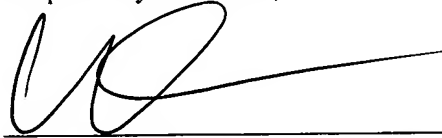
Pursuant to 37 C.F.R.1.97(h), the submission of this Information Disclosure Statement is not to be construed as an admission that the information cited in this statement is material to patentability.

The Commissioner is hereby authorized to charge any fee deficiency in connection with this submission to Deposit Account No. 501914.

Respectfully submitted,

Date

12/7/2006



William L. Paradice, Reg. No. 38,990
Tel. 408-236-6646

Substitute for Form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Page 1 of 5

Application No.

10/613,776

Filed

7/2/2003

First Inventor

Srinivasan, Varadarajan

Art Unit

2661

Examiner**Atty. Docket No.**

NLMI.P213

**Non Patent Literature Documents**

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	Agere Ads Additional PaloadPlus Processor to Product Line, Agere Press Release, November 29, 2000 (11/29/00), 3 pgs.	
	Architecture and Design of Function Specific Wire-Speed Routers for Optical Internetworking, published by Entridia Corp., December 6, 2000 (12/6/00), 60 pgs.	
	ATLAS I: A General-Purpose, Single-Chip ATM Switch with Credit-Based Flow Control, IEEE Hot Interconnects IV Symposium Proceedings, Stanford, CA, Pub. August 15-17, 1996, 11 pgs.	
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw a line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.

Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT Page 2 of 5	Application No.	10/613,776
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	PMC-Sierra's ATM Chip Set Provides the Traffic Management and Switch Fabric Core for Ericsson's AXD 301 ATM Switch, PMC-Sierra, Publ. December 12, 2000 (12/12/00), 10 Pgs.	
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	Start-time Queuing: A Scheduling Algorithm for Integrated Services Packet Switching Networks, Pawan Goyal, Harrick M. Vin and Haichen Cheng, Distributed Multimedia Computing Laboratory, Dept. Computer Sciences, University of Texas at Austin, Pub. 1996, 12	

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	Wire Speed Quality of Service Over Ethernet, Switchcore, Pub. May 8, 2000 (5/8/00), 19 pgs.	

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